Vipul J. Patel is a microelectronics leader with a career spanning industry, government, and defense. At AFRL, he drove innovation in secure design, trusted IP, and workforce development. He advised DoD leadership through the DMCFT and now leads federal semiconductor strategy and development at Ansys, Inc. A published researcher with four patents, he is deeply engaged in RF, analog, digital, and mixed-signal systems; microelectronics assurance; electronic design automation; and national security microelectronics. Dr. Patel served as 2025 General Chair and 2024 Technical Chair of GOMACTech and contributed to DARPA, NSF, and AFOSR initiatives. He is a Senior Member of IEEE and an active technical reviewer.